

Patent Assignment Abstract of Title

Total Assignments: 1

Application #: 10054687 Filing Dt: 01/22/2002 Patent #: NONE Issue Dt:

PCT #: NONE Publication #: <u>US20030139927</u> Pub Dt: 07/24/2003

Inventors: Thaddeus J. Gabara, Inkyu Lee, Maria Luisa Lopez-Vallejo, Syed Mujtaba

Title: Block processing in a maximum a posteriori processor for reduced power consumption

Assignment: 1

 Reel/Frame:
 012779 / 0656
 Received:
 Recorded:
 Mailed:
 Pages:

 04/16/2002
 04/11/2002
 06/10/2002
 11

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: GABARA, THADDEUS J. Exec Dt: 01/23/2002

«<u>LEE, INKYU</u>

LOPEZ-VALLEJO, MARIA EUISA Exec Dt: 03/20/2002

MUJTABA-SYED.A. Exec Dt: 01/30/2002

Assignee: AGERE-SYSTEMS_INC.

555 UNION BOULEVARD

ALLENTOWN, PENNSYLVANIA 18109

Correspondent: MENDELSOHN-&-ASSOCIATES, P.C.

STEVE MENDELSOHN 1515 MARKET STREET

SUITE 715

PHILADELPHIA, PA 19102

DONE

Exec Dt: 01/22/2002

Search Results as of: 11/18/2005 1:54:00 P.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 571-272-3350 Web interface last modified: September 28, 2005